



Le génie pour l'industrie

Real-time phasor simulation test-bed
for secondary voltage control of power
grids using wide-area measurements



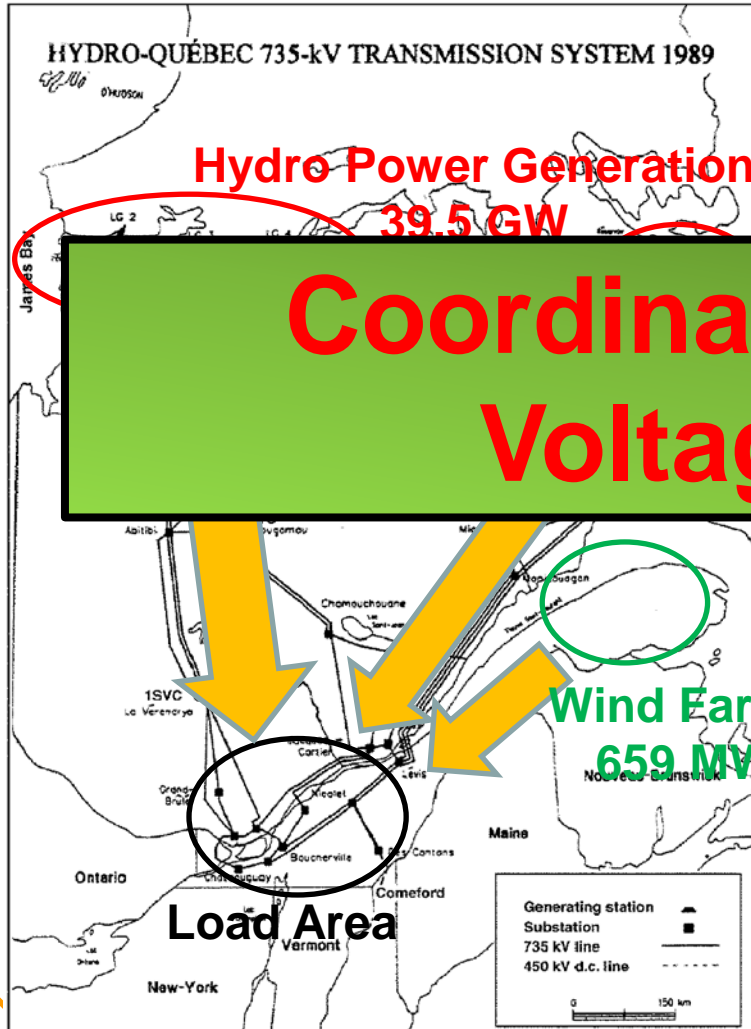
Outline:

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Problem statement

Overview of Hydro Quebec Network



Coordinated Secondary Voltage Control

Long distance

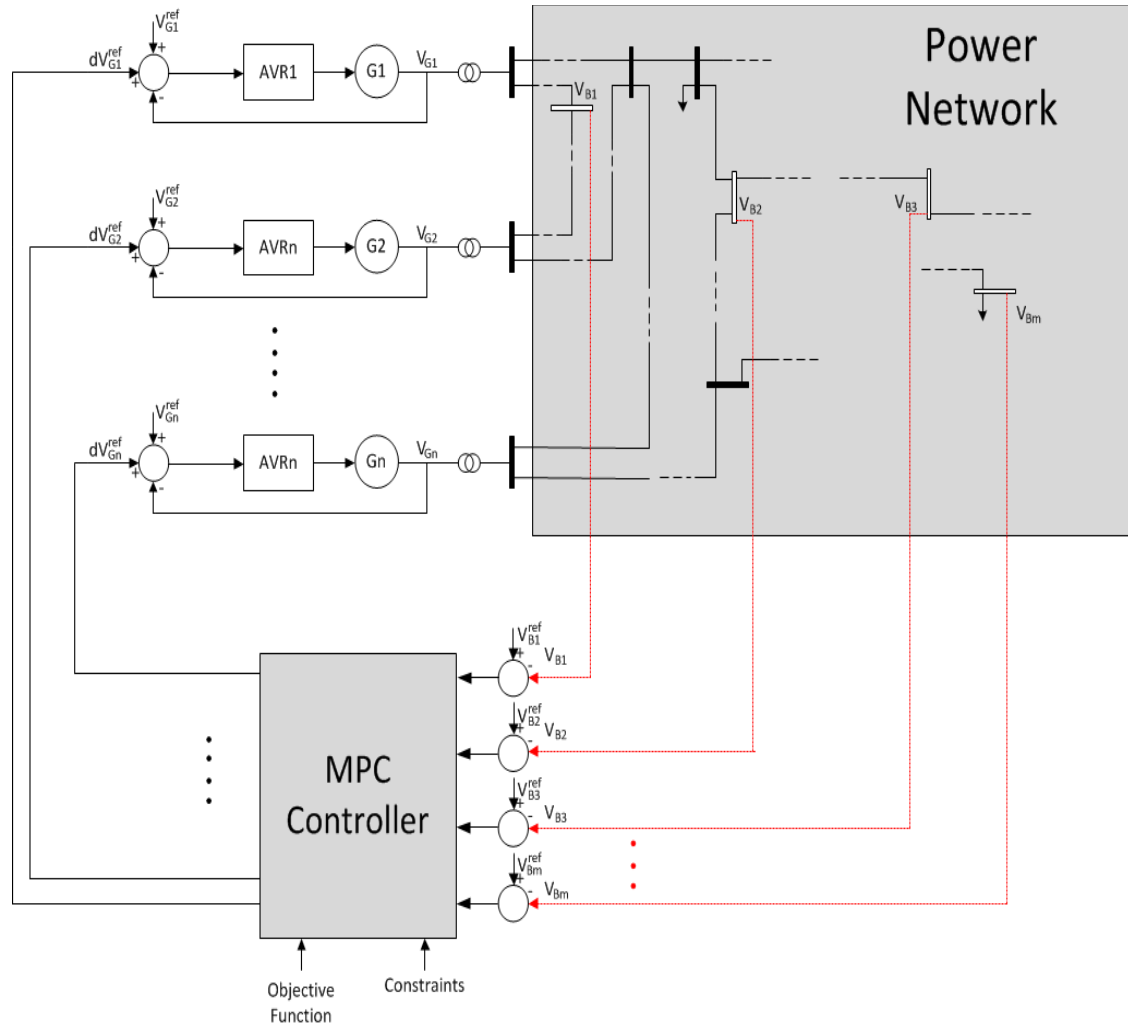
- Large amount of reactive power loss
- Voltage drops
- Voltage instability

Manual operator based voltage control in secondary level

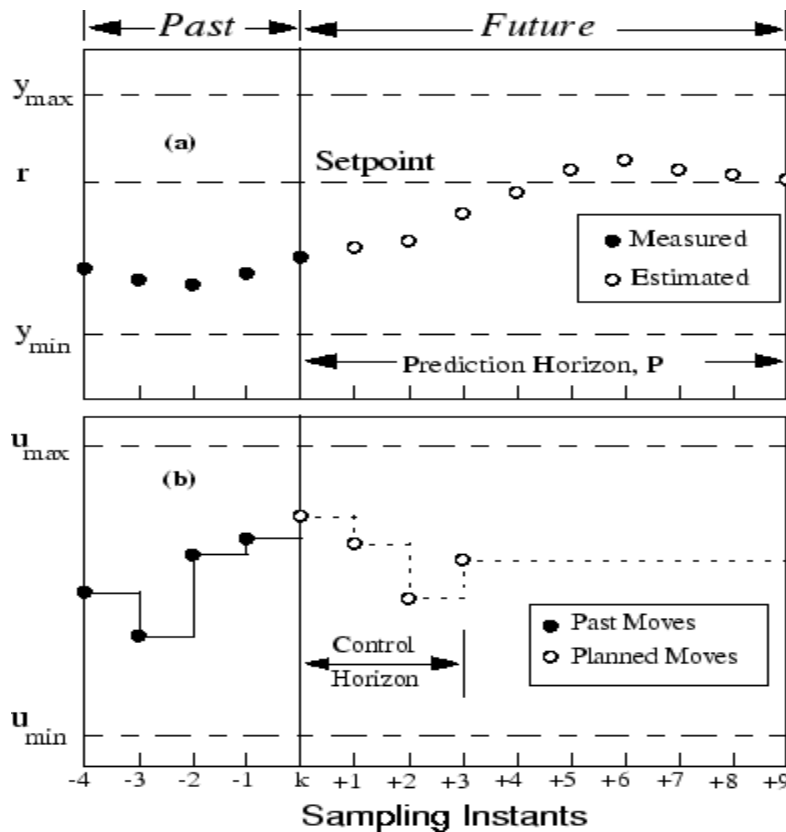
- Based on off-line calculations and pre-defined schedules
- No coordination between reactive power compensators

Secondary level Voltage Control

- Voltage regulation and tracking at sensitive buses called pilot nodes using:
 - Changing V_{ref} of exciters on the machines
 - Changing V_{ref} or Q_{ref} of Static Var compensators
 - Switching capacitor/inductor banks
 - ...
- Consider constraints: voltage of the buses and MVAR limits
- Time step of the controller: 10sec.
- Settling time (for 3%) in 1min



MPC Algorithm:



- 1) State estimation based on measured output and previous input/output set.
- 2) Use Identified LTI model to relate outputs for next P future steps, to next M future values of the inputs ($M \leq P$). In this way we will have P equation and M unknown. P and M are prediction and control horizons respectively.
- 3) Solve Optimization problem with respect to unknown inputs in presence of given constraints

$$\min_{\Delta u(k), \dots, \Delta u(k+m-1)} \left\{ \sum_{j=0}^{m-1} \Delta u^T(k+j) R \Delta u(k+j) \right. \\ \left. + \sum_{i=0}^{p-1} \Delta y^T(k+i|k) Q \Delta y(k+i|k) + \rho \varepsilon^2 \right\}$$

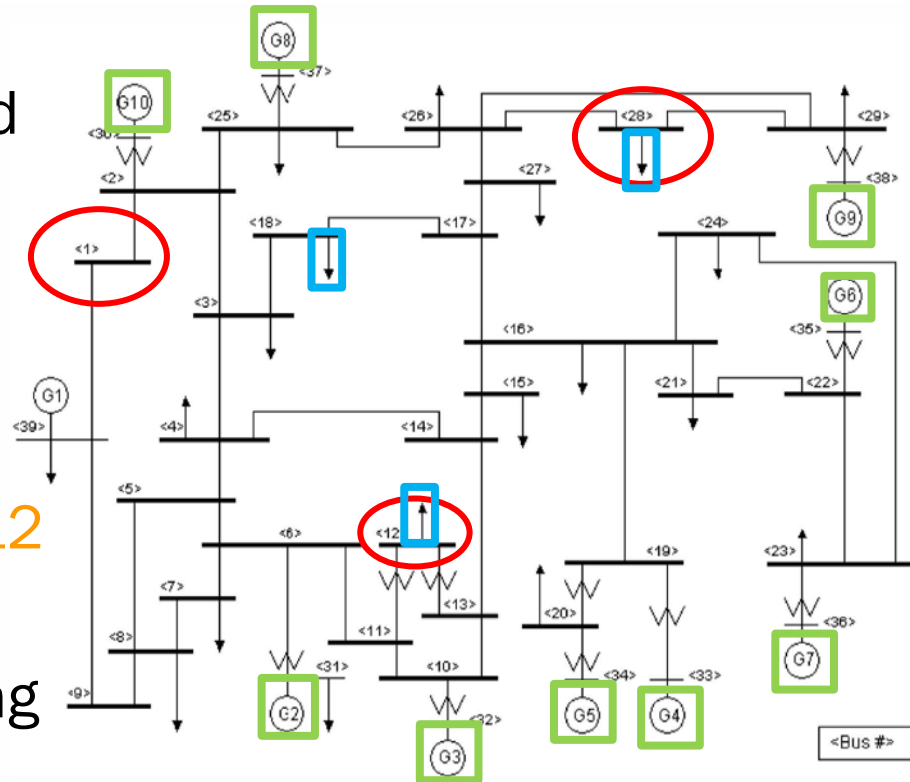
Subject to:

$$\begin{cases} u_{\min} \leq u(k+j) \leq u_{\max} & j = 0, \dots, m-1 \\ \Delta u_{\min} \leq \Delta u(k+j) \leq \Delta u_{\max} & j = 0, \dots, m-1 \\ y_{\min} - \varepsilon \leq y(k+i|k) \leq y_{\max} + \varepsilon & i = 0, \dots, p-1 \\ \Delta u(k+l) = 0 & l = m, \dots, p-1 \\ \varepsilon > 0 \end{cases}$$

- 4) Apply the first element of control signal obtained from the optimization procedure.
- 5) Go to step 1 for next sampling time, $k+1$

Simulation Test Case

- IEEE39 bus system used as test case
- **pilot buses** to install PMUs: **buses 1,12 & 28**
- Identify linear model of the system: **12 states, 12 inputs, 3 outputs**
- Controller designed using **MPC toolbox** in MATLAB

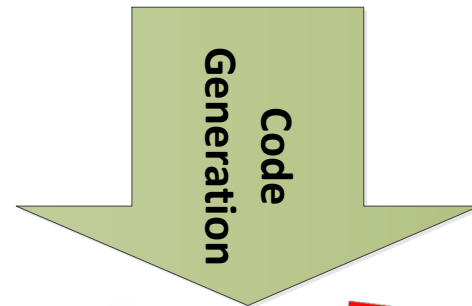
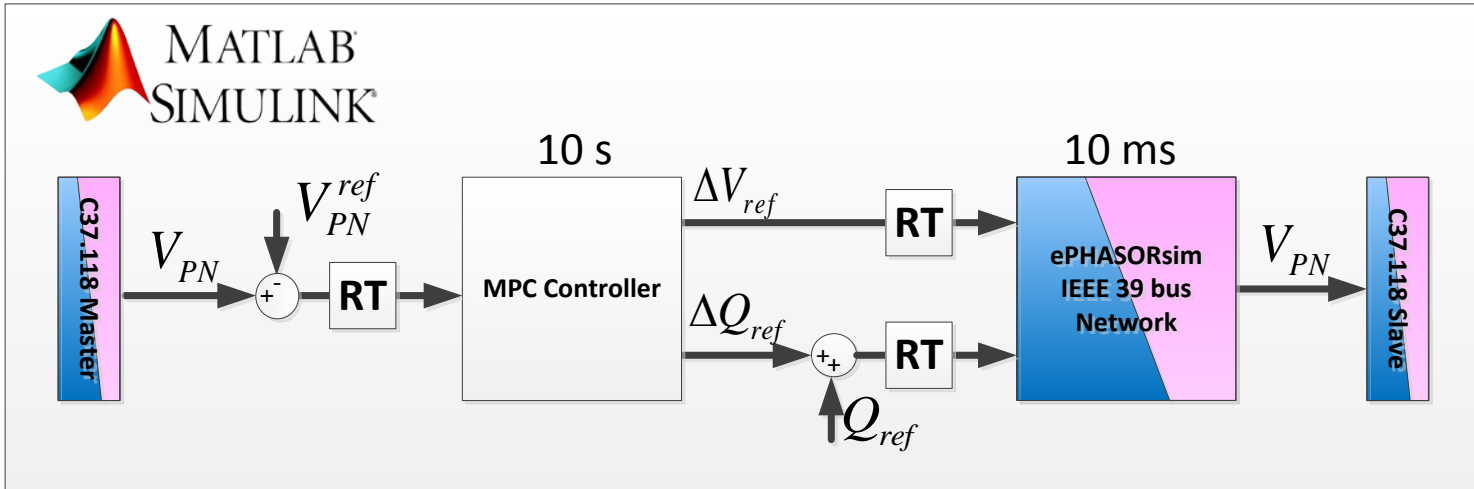


Selected Pilot buses

Change of V_{ref}

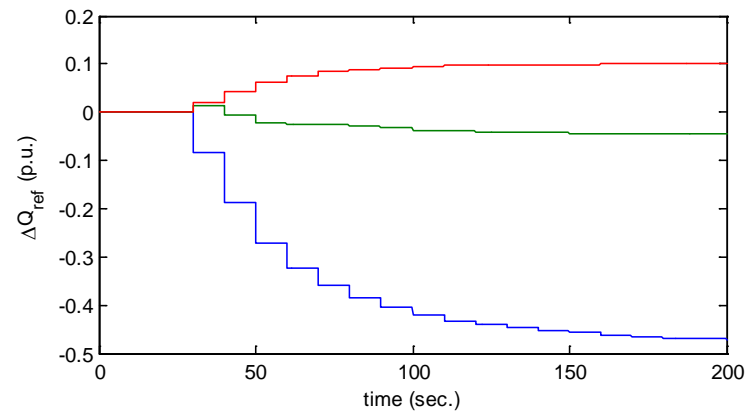
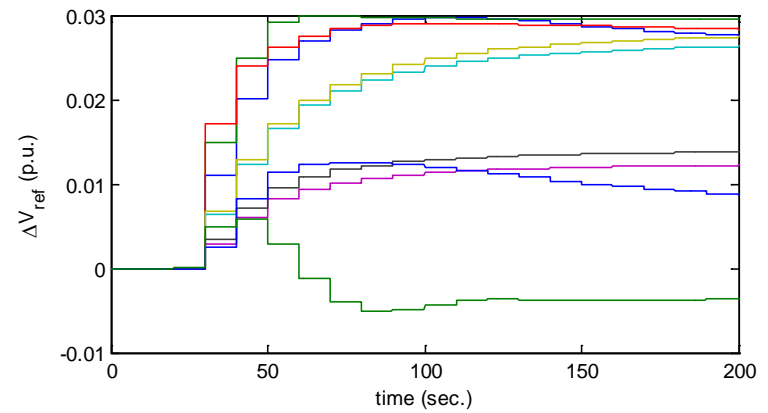
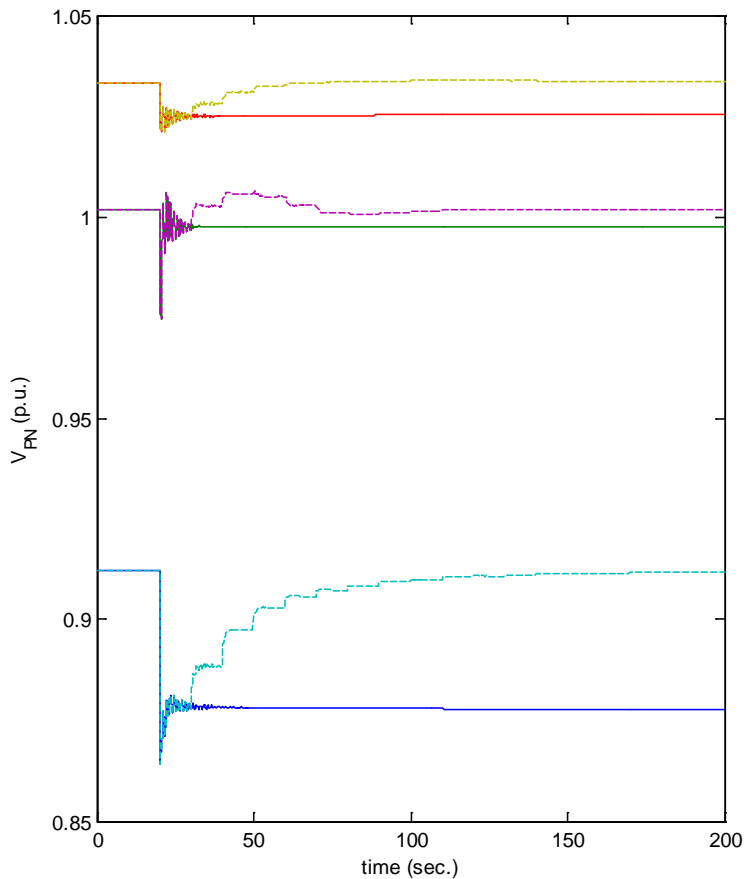
Change of Q_{ref}

Real-time testbed



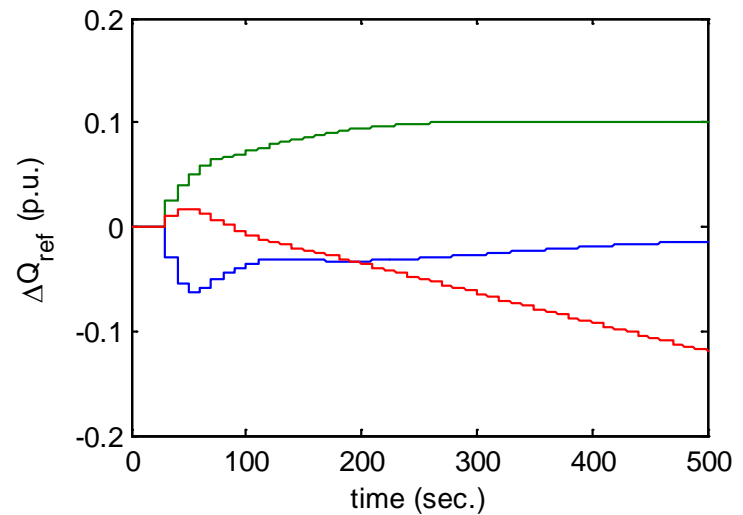
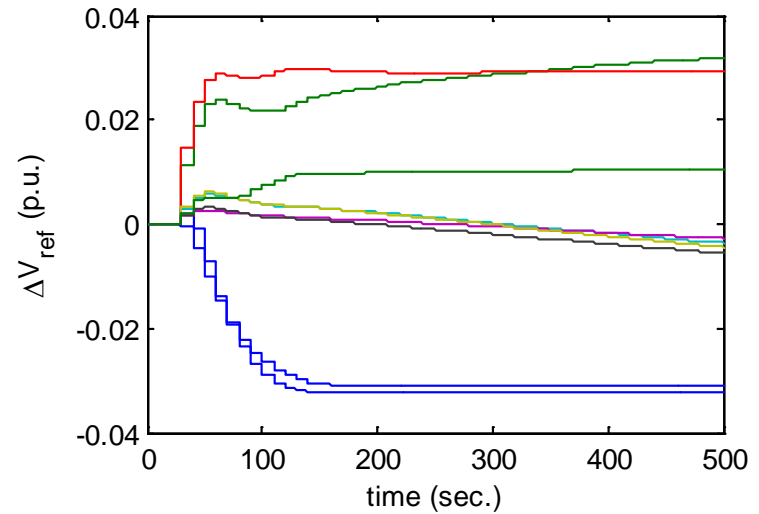
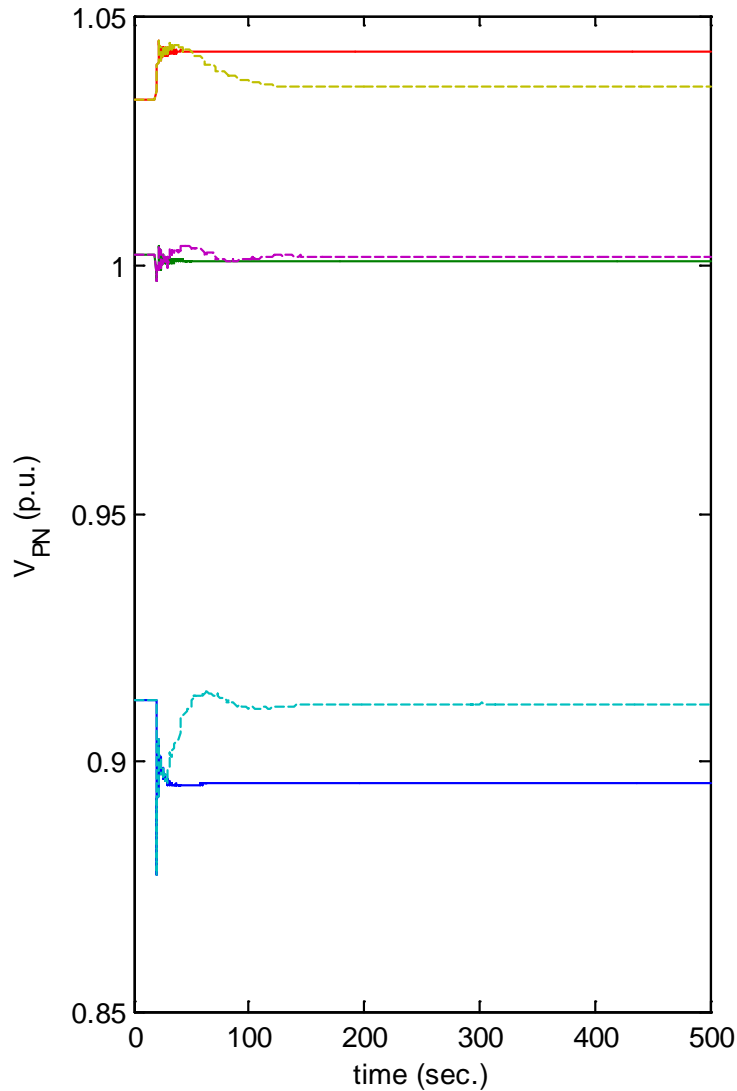
Simulation Results: Voltage Regulation

Trip generator: G3 is tripped at t=10s



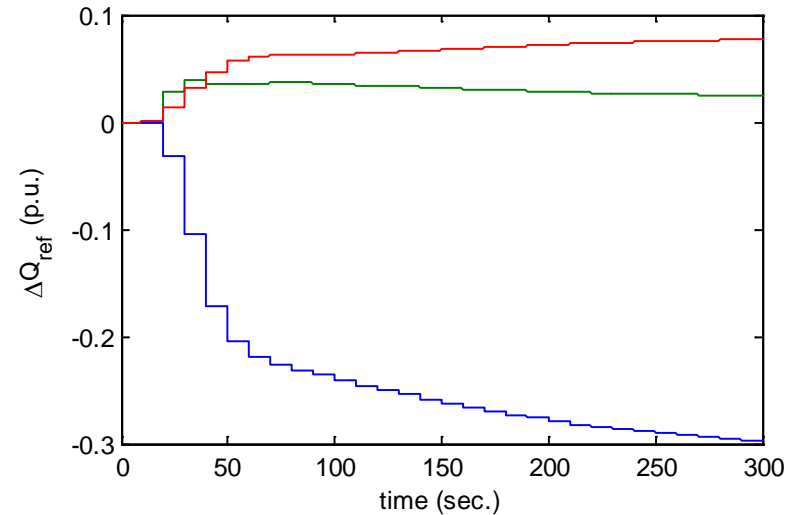
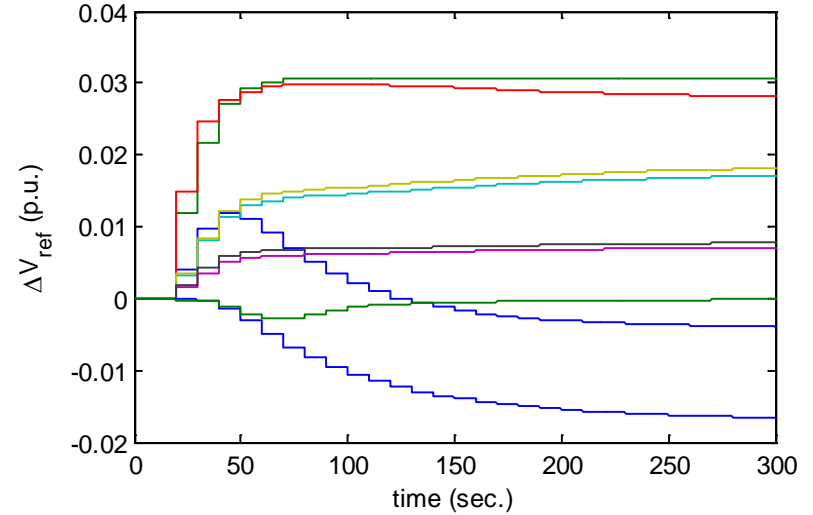
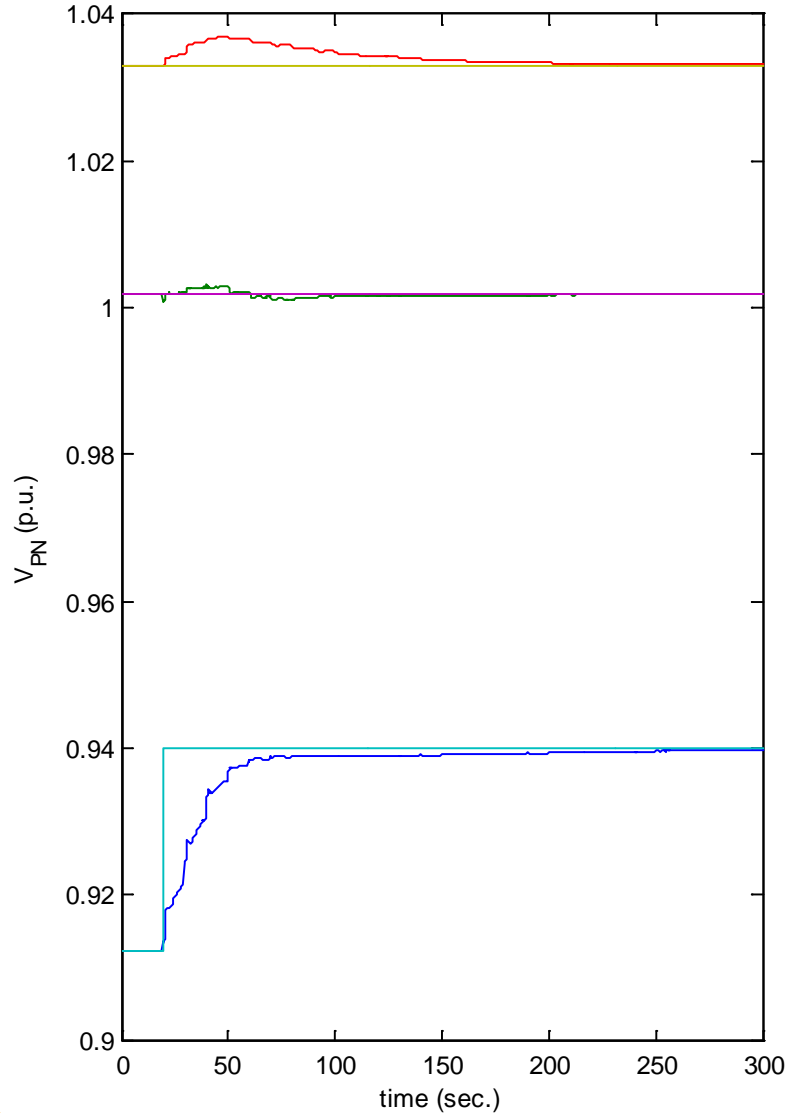
Simulation Results: Voltage Regulation

Trip Line: Bus 8 to Bus 9



Simulation Results: Voltage Tracking

Change Vref of Bus12 from 0.923p.u to 0.94p.u



Conclusion and future works

- MPC controller can handle voltage regulation and tracking at pilot buses in presence of disturbances.
- A real-time validation is necessary for control algorithms such as MPC who requires time for calculations of the control input.
- For larger scale networks, Centralized MPC computational burden may go beyond sample time of the controller. Decentralized MPC approach can be used as an alternative.

Acknowledgement

- I would like to thank OPAL-RT technologies, and specifically ePHASORSim team who helped me to accomplish this project.
- THANK YOU ALL

Question?



ÉTS

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École de technologie supérieure

L'ÉTS est une constituante du réseau de l'Université du Québec